

Figure 1A (Prior Art)

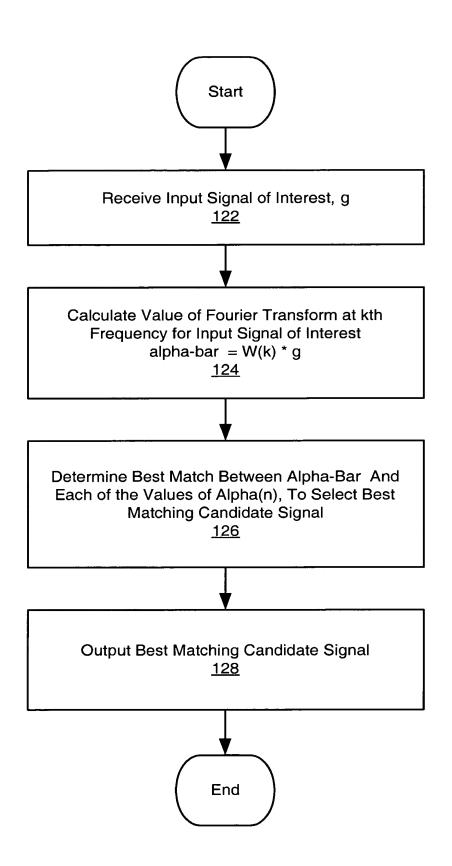
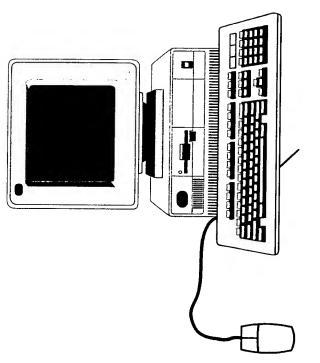


Figure 1B (Prior Art)



Host Computer System 202

Figure 2

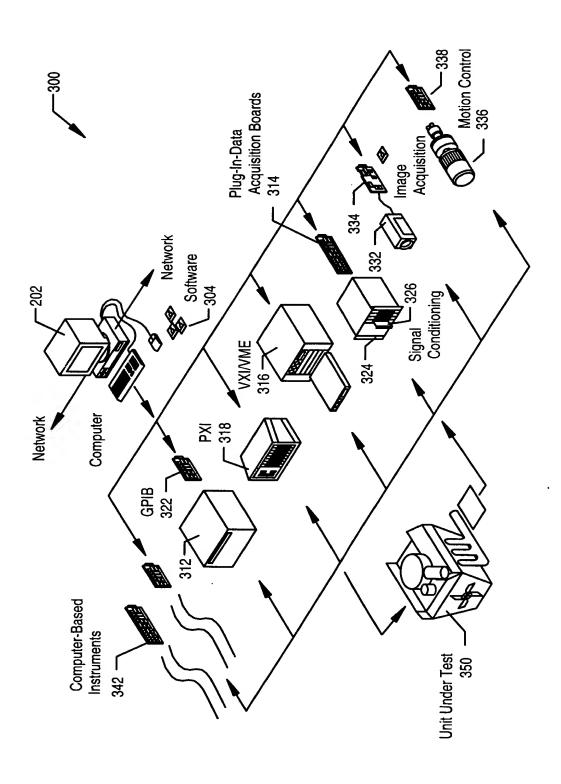


FIG. 3A

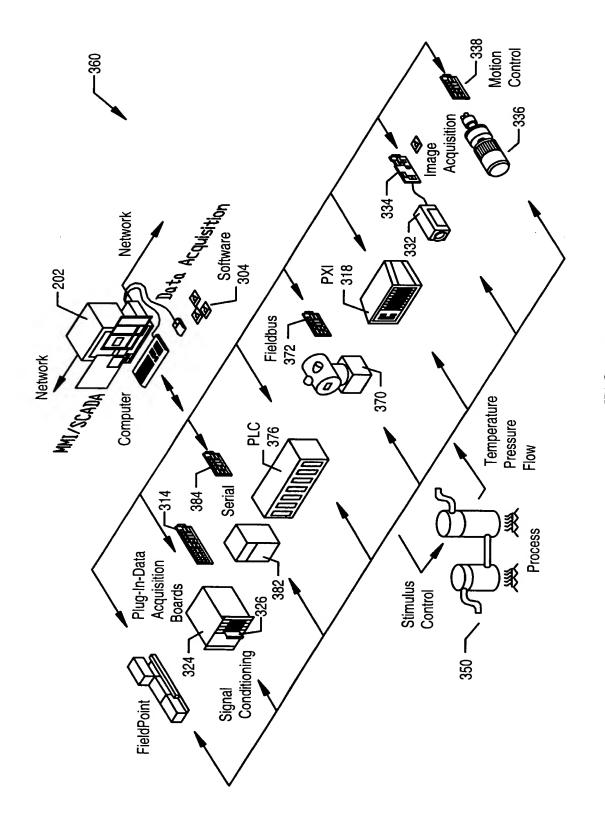


FIG. 3B

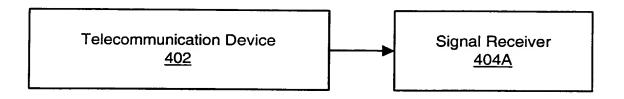


Figure 4A

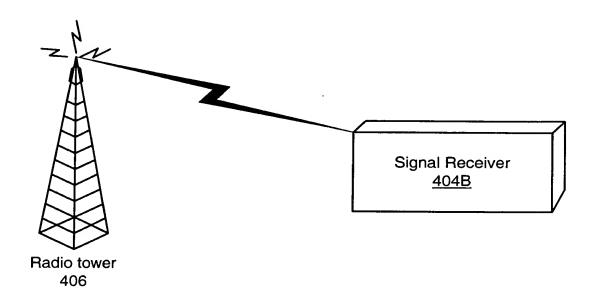


Figure 4B

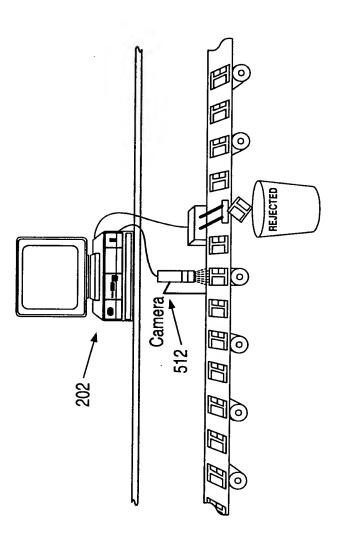


Figure 5

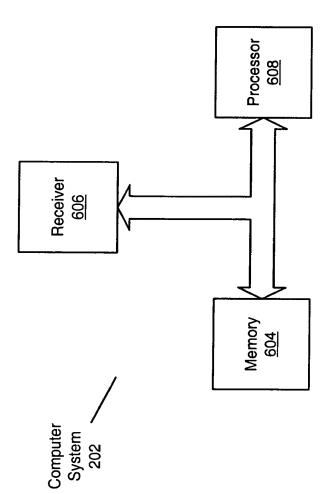


Figure 6

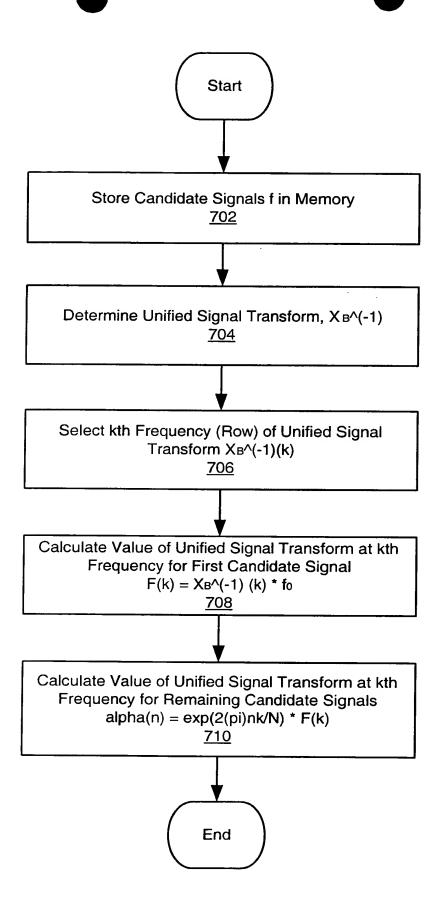


Figure 7A

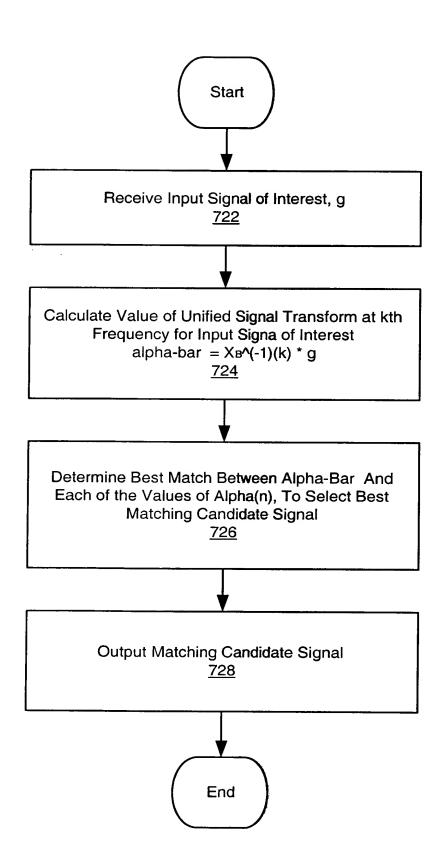


Figure 7B

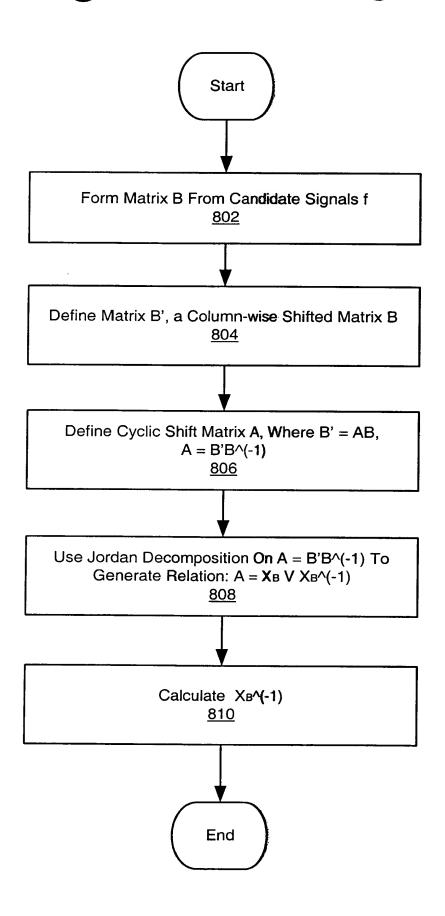
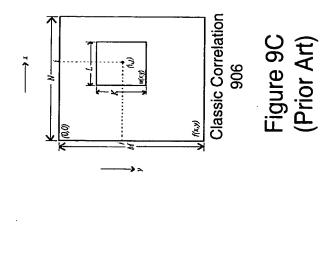


Figure 8



Template Image, It 904 Figure 9B

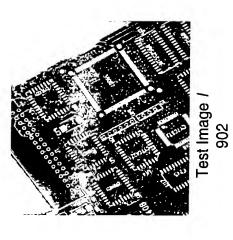
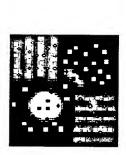


Figure 9A



Reference Pattern 1002



Sampled Pattern 1004



Shift Invariant Pattern Matching

Figure 10

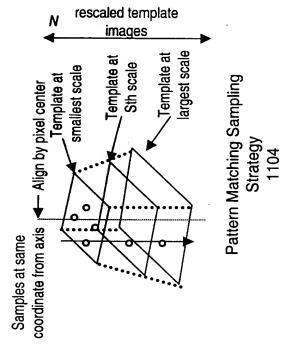


Figure 11B

 f_p f_p f_p g(k) UST Circle 1102

Figure 11A